CHOI Q64401
APPARATUS FOR COMPENSATING FOR PHASE
DIFFERENCE ATTENDANT UPON TIME DIVISION
MULTIPLEXING AND METHOD THEREOF
Filed: July 3, 2001
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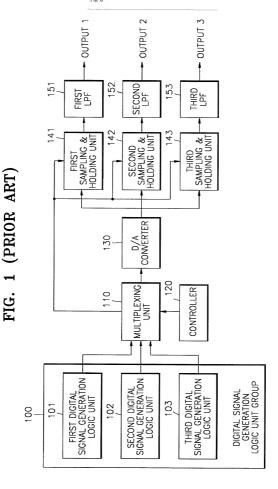
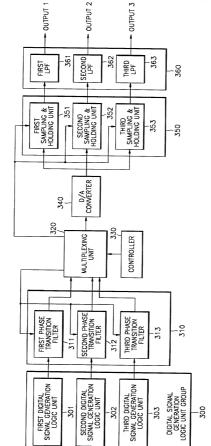


FIG. 2 (PRIOR ART)

			-			***				
	X A0 X A1 X A2 X A3 X	X 80 X 81 X 82 X 83 X	X C0 X C1 X C2 X C3 X	XA0XB0XC0XA1XB1XC1XA2XB2XC2XA3XB3XC3X	-(A0) (A2) (A3)			—(A0)——(A1)——(A2)———(A3)————————————————————————————————		(CO)(C1)(C2)(C3)
	FIRST DIGITAL SIGNAL GENERATION LOGIC UNIT	SECOND DIGITAL SIGNAL GENERATION LOGIC UNIT	THIRD DIGITAL SIGNAL GENERATION LOGIC UNIT	MULTIPLEXING UNIT	FIRST SAMPLING & HOLDING UNIT	SECOND SAMPLING & HOLDING UNIT	THIRD SAMPLING & HOLDING UNIT	FIRST LPF	SECOND LPF	THIRD LPF

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FIG. 3

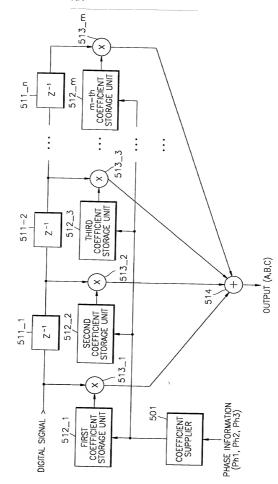
-	A1	
٠.		
FIG	AO	
	AP F	
	FIRST DIGITAL SIGNAL INERATION LOGIC UNIT	
	F.F.	

X A2 X A3 X	X B2 X B3 X	X C2 X C3 X	(A2) (A3)	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	(C1") (C3")	(C1"X A2 XB2 XC2"X A3 XB3 XC3"X	—(A2)——(A3)—————————————————————————————————		C1"	—(A2)——(A3)—————————————————————————————————	(B2)(B2) $$	C1"(C2)(C2"(C3"(C3)(C3")
X A0 X A1 X A2	X B0 X B1 X B2	X CO X C1 X C2	—(A0)——(A1)——(A2)——		(CO")	X 40 \\B0'\C0"\X 41 \\B1'\\C1"\X 42 \\B2 \\C2"\X 43 \\B3'\C3"\	—(A0)——(A1)——(A2)—		(00)	—(A0)——(A1)——(A2)——	$-(B0)\overline{B0'}\}(B1)\overline{B1'}\}(B2)\overline{B2'}\}$	<u> </u>
FIRST DIGITAL SIGNAL GENERATION LOGIC UNIT	SECOND DIGITAL SIGNAL GENERATION LOGIC UNIT	THIRD DIGITAL SIGNAL GENERATION LOGIC UNIT	FIRST PHASE TRANSITION FILTER	SECOND PHASE TRANSITION FILTER	THIRD PHASE TRANSITION FILTER	MULTIPLEXING UNIT	FIRST SAMPLING & HOLDING UNIT	SECOND SAMPLING & HOLDING UNIT	THIRD SAMPLING & HOLDING UNIT	FIRST LPF	SECOND LPF	THIRD LPF

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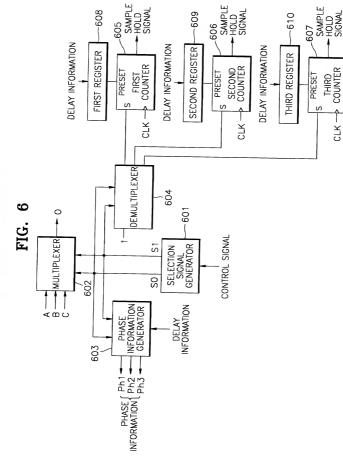
FIG. 5

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THIRD COUNTER

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POHOZOZO, TRZZesoo